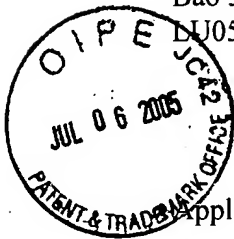


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Bao 37-49-1
DU05018USU

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Bao et al.

Serial No.: 10/671,303

Filed: September 24, 2003.

For: SEMICONDUCTOR DEVICES HAVING REGIONS OF INDUCED HIGH
AND LOW CONDUCTIVITY, AND METHODS OF MAKING THE SAME

Group: 2829

Examiner: David A. Zarneke

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date set forth below:

Signed: 

Name: Jay M. Brown

Date: July 1, 2005

Durham, North Carolina
July 1, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

This Supplemental Information Disclosure Statement (IDS) is being filed to make of record several documents that have been identified in a European Search Report dated April 1, 2005 in the European Patent Application Serial No. 04256846, the U.S. counterpart of which has been incorporated by reference in this application. A copy of the European Search Report is enclosed for the convenience of the Examiner. This Supplemental IDS is being mailed within 3 months of receipt of the documents now being cited.

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicants' attorney wishes to bring to the attention of the Patent & Trademark Office the following items listed on the

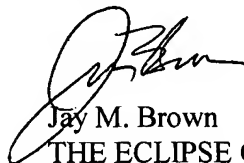
accompanying Form 1449. Each of these items was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.

ITEMS

1. Smith et al., U.S. Patent Application Publication No. 2003/0175551A1, entitled "Surface Modified Organic Thin Film Transistors", published on 9/18/2003.
2. Qin, Dong et al., "Fabrication of Ordered Two-Dimensional Arrays of Micro- and Nanoparticles Using Patterned Self-Assembled Monolayers as Templates", *Adv. Mater.*, Vol. 11, No. 17, pp. 1433-1437 (1999).

The filing of this IDS shall not be construed as a representation that a search has been made nor shall it be construed as an admission that the information cited is, or is considered to be, material to patentability, nor shall it be construed as a representation that no other material information exists.

Respectfully submitted,



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FORM 1418-1449 U.S. Department of Commerce

Patent and Trademark Office

List of Documents Cited by Applicant

Attorney Docket No.:

LU05018USU (Bao 37-49-1)

Serial No.:

10/671,303

Applicant(s): Bao et al.

Filing Date: September 24, 2003

Group: 2829

U.S. PATENT DOCUMENTS

| Examiner Initials | No. | Document Number | Date | Name | Class | Subclass | Filing date if Appropriate |
|----------------------|-----|--------------------|------|------|-------|----------|-------------------------------|
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FOREIGN PATENT DOCUMENTS

| Examiner Initials | No. | Document Number | Date | Name of Patentee or Applicant | Country | Translation Yes No |
|----------------------|-----|--------------------|------|-------------------------------|---------|-------------------------|
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

| Examiner Initials | No. | Full Information Of Document |
|----------------------|-----|---|
| | 01 | Smith et al., U.S. Patent Application Publication No. 2003/0175551A1, entitled "Surface Modified Organic Thin Film Transistors", published on 9/18/2003. |
| | 02 | Qin, Dong et al., "Fabrication of Ordered Two-Dimensional Arrays of Micro- and Nanoparticles Using Patterned Self-Assembled Monolayers as Templates", <i>Adv. Mater.</i> , Vol. 11, No. 17, pp. 1433-1437 (1999). |
| | | |

EXAMINER _____ DATE CONSIDERED _____

*Examiner Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.